UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,958,590 B1

Page 1 of 1

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INVENTOR(S)

: Jarmo Antero Vaananen

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Add Figure 1 to the face page of patent

Column 3, Line 63: "current source 12" should read --current source I2--

Column 3, Line 63: "current source 13" should read --current source I3--

Column 3, Line 67: "(12-14) is controlled" should read--(I2-I4) is controlled

Column 3, Line 67: "sense circuit (I)" should read --sense circuit (I1)--

Signed and Sealed this

Nineteenth Day of September, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office